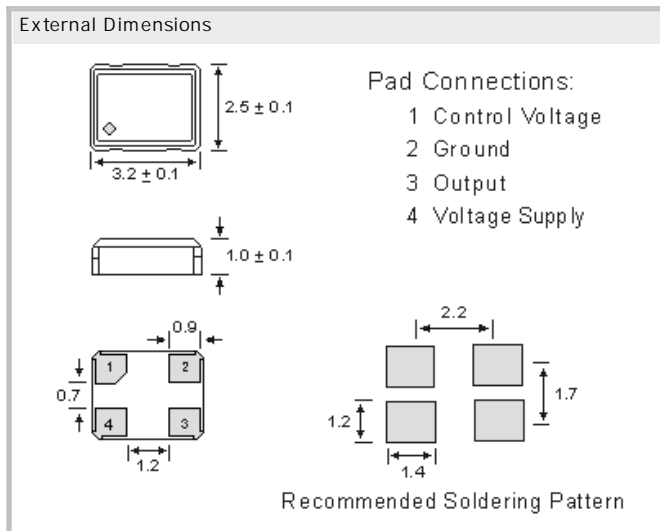
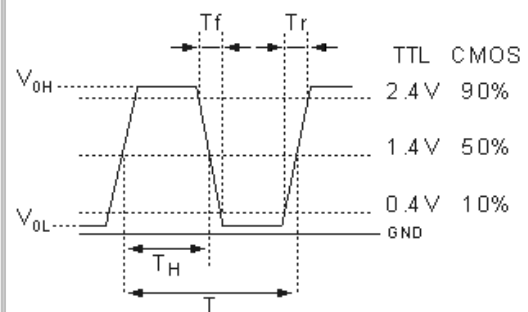


VCXO-Oscillator SMD VCXO3225T3.3-4pad 3.3V  
Voltage Controlled Crystal Oscillator

- Output Wave Form CMOS/TTL
- SMD in ceramic case (3.2 x 2.5 x 1.0) mm
- RoHS conform; Lead-free product
- Vibration: MIL-STD-202F method 204, 35G, 50 to 2000 Hz
- Shock: MIL-STD-202F method 213B, test cond. E, 1000GG 1/2 sine wave
- Available in many standard and special frequencies



CMOS/TTL Output Wave Form



## Specifications - Product No. G027000000JQSUPN43AB

Holder Type:	VCXO3225T3.3-4pad 3.3V (Voltage code is "3.3")
Frequency:	27.000000 MHz
Initial Freq. Accuracy (at 25 °C):	To tune to the nominal frequency with $V_c = 1.65V \pm 0.2V$
Freq. Stability o. Operating Temp. Range:	$\pm 25.0$ ppm
Operating Temperature Range:	$\pm 25.0$ ppm over $-40^\circ\text{C}$ to $+85^\circ\text{C}$ (inclusive of $25^\circ\text{C}$ tolerance, $\pm 10\%$ input voltage variation, load change, aging, shock and vibration)
Frequency Deviation:	$\pm 140$ ppm
Power Supply Voltage (Vdd):	+3.3V DC $\pm 10\%$
Maximum Supply Current:	20.0mA max.
Output Load CL:	2 TTL gates max. / CMOS 15 pF
Output "1" Level (VOH):	2.4V (min.) TTL   2.97V (min.) CMOS
Output "0" Level (VOL):	0.4V (min.) TT   0.33V (min.) CMOS
Output Symmetry (Duty Cycle):	45/55%
Voltage Control:	0.9V DC Center / 0.00V to 1.8V Range
Linearity:	6% typical; 10% max.
Rise/Fall Time:	6ns (max.) 4ns (typ.) Measured between 0.4V and 2.4V
Input Impedance:	5M $\Omega$ min.
Integrated Phase Jitter:	1 ps max. (12 kHz to 20 MHz)
Phase Noise (27MHz at 3.3V):	-75dBc/Hz at 10Hz offset -145dBc/Hz at 10kHz offset -104dBc/Hz at 100Hz offset -152dBc/Hz at 100kHz offset -132dBc/Hz at 1kHz offset -150dBc/Hz at 1MHz offset
Start Up Time:	10 ms (max.), 5ms (typ.)
Aging:	$\pm 3$ ppm per year (max.)
Reflow Condition:	10 sec. max. at $260^\circ\text{C}$

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