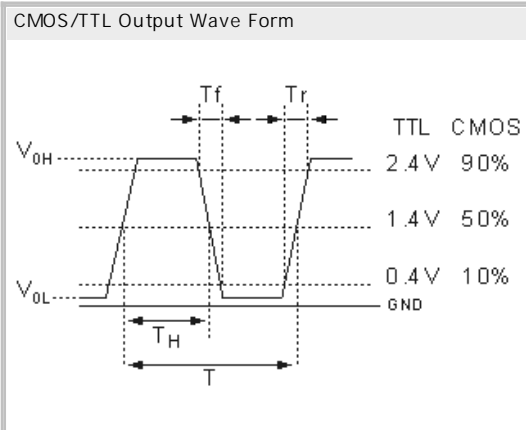
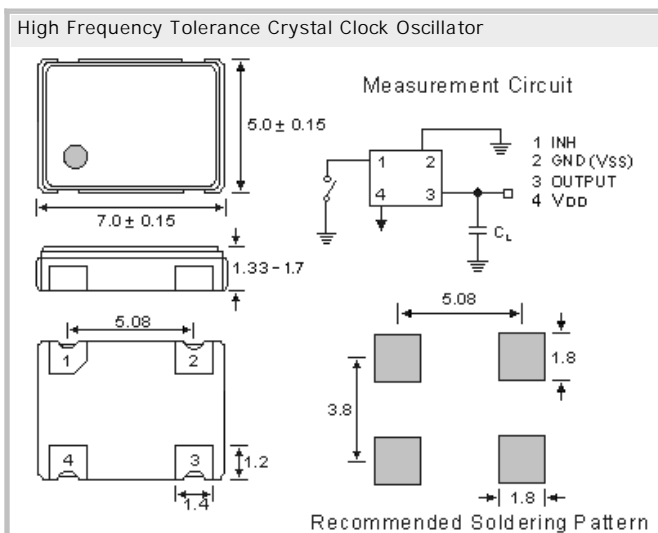


High Frequency Tolerance
Crystal Clock Oscillator CXO7050TR3.3 3.3V

- SMD in ceramic case (7.0 x 5.0 x 1.8) mm
- Tri-State Enable / Disable
- LVCMOS Square Wave
- on Tape & Reel (Tape 16mm)
- RoHS conform; Lead-free product
- Vibration: MIL-STD-202F method 204, 35G, 50 to 2000 Hz
- Shock: MIL-STD-202F method 213B, test cond. E, 1000G 1/2 sine wave
- Available in many standard and special frequencies



Specifications

| | |
|-----------------------------------|--|
| Holder Type: | High Frequency Tolerance Crystal Clock Oscillator CXO7050TR3.3 Tri-State 3.3V (Voltage code is " 3.3 ") LVCMOS |
| Frequency: | 50.000000 MHz |
| Frequency Tolerance at 25°C: | ± 2.0 ppm (max.) |
| Operating Temperature Range: | ± 15.0 ppm over -40°C to +85°C (inclusive of 25°C tolerance, ± 10% input voltage variation, load change, aging, shock and vibration) |
| Storage Temperature: | -55°C to +150°C |
| Power Supply Voltage (Vdd): | + 3.3V D.C. ± 5 % |
| Maximum Supply Current: | 7.0 mA max. |
| Output Load: | 15pF |
| Output Symmetry (Duty Cycle): | 50% ± 5% (measured at 50% Vdd) |
| Output Logic Levels: | High "1" Vdd-0.4(min) // Low "0" 0.1V (typical), 0.4V (max.) |
| Rise/Fall Time (10% to 90% Vdd): | 2 ns (typical) < 4 ns max. |
| Tri-state Function on Pin 1: | Pin 1 = H or open.... Output active at pin 3 Pin 1 = L.... high impedance at pin 3 |
| Phase Jitter (RMS) [25MHz, 3.3V]: | 300fs (typical) [12kHz to 20MHz integrated] |
| SSB Phase Noise [25MHz, 3.3V]: | -65dBc/Hz @ 10Hz, -100dBc/Hz @ 100Hz, -130dBc/Hz @ 1kHz -145dBc/Hz @ 10kHz, -148dBc/Hz @ 100kHz, -152dBc/Hz @ 1MHz, -155dBc/Hz @ 10MHz |
| Aging: | < ±2ppm max. / for the first year |
| Reflow Condition: | 260°C max for 10 sec. |

GERMANY:

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