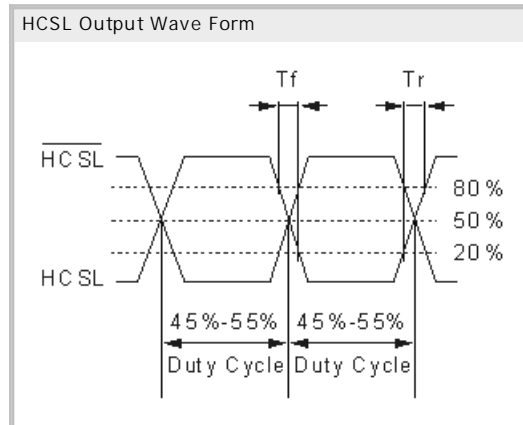
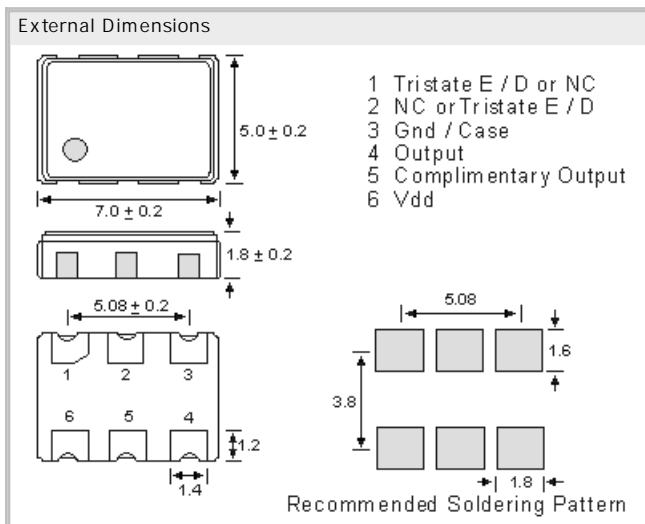


Differential HCSL Clock Oscillator, no PLL
CXO7050CK 3.3, 3.3V, 200 fsec Jitter

- SMD in ceramic case (7.0 x 5.0 x 1.8) mm
- Tri-State Enable / Disable on pad No. 1
- Fem to second integrated phase jitter (200 fs typical, 12 KHz to 20 MHz)
- Superior phase noise (-138 dBc/Hz at 10 KHz and -144 dBc/Hz at 100 KHz offset)
- RoHS conform; Lead-free product; on Tape (16mm) & Reel
- Vibration: MIL-STD-202F method 204, 35G, 50 to 2000 Hz
- Shock: MIL-STD-202F method 213B, test cond. E, 1000GG 1/2 sine wave
- High performance with surprisingly low price



Specifications

| | |
|-------------------------------------|--|
| Holder Type: | CXO7050CK 3.3; 3.3V (Voltage code is "3.3"); Tri-State on pad 1 |
| Frequency: | 100.000000 MHz |
| Frequency Stability at 25°C: | ± 50.0 ppm |
| Operating Temperature Range: | ± 50.0 ppm over -40°C to +85°C (inclusive of 25°C tolerance, ±10% input voltage variation, load change, aging, shock and vibration) |
| Storage Temperature: | -55°C to +150°C |
| Power Supply Voltage (Vdd): | + 3.3V D.C. ± 5% |
| Maximum Supply Current (15pF load): | 25.0 mA max. |
| Output Swing: | 620 mV min; 700 mV typical; 780 mV max. |
| Output Logic Levels: | High "1" Voh 660 mV min.; 740 mV typical; 850 mV max. Low "0" Vol -150 mV min.; 0 mV typical; 150 mV max. |
| Output Symmetry (Duty Cycle): | 50% ± 5% max. measured at Q and complimentary Q cross point |
| Load: | RL = 50 Ohm to ground on each output |
| Rise/Fall Time: | 0.15 ns. typ; 0.4 ns. max. 20% -> 80% of HCSL waveform/(RL = 100 , CL = 10pF) |
| Start Up Time: | 3 ms typical; 10 ms max. |
| Tri-state Function Pin 1: | If not connected or 2.4V min. (referenced to ground) is applied: Output. Internal pull-up. Oscillation disable time is 0.2 µs max. If 0.6V max. is applied: High impedance. Current consumption is 10 µA max. Oscillation enable time is 2ms. max. |
| Phase Jitter (12 kHz to 20 MHz): | 200 fs typical for 155.52 MHz |
| Phase Noise (125 MHz): | -50dBc/Hz @ 10Hz, -82dBc/Hz @ 100Hz, -116dBc/Hz @ 1kHz -138dBc/Hz @ 10kHz, -144dBc/Hz @ 100kHz, -149dBc/Hz @ 1MHz, -155dBc/Hz @ 10MHz |
| Aging: | < ± 3ppm max. for the first year |
| Reflow Condition: | 260°C max for 10 sec. |

GERMANY:

COMTEC CRYSTALS GmbH · Sultenstrasse 12-14
8 5 5 8 6 P o i n g / G E R M A N Y
Phone +49 8121 778160 · Fax +49 8121 778177
e-Mail info@comtec-crystals.com
Internet: <http://www.comtec-crystals.com>
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FRANCE:

COMTEC CRYSTALS SARL · 23, rue du Faucon
6 7 5 0 0 H a g u e n a u / F R A N C E
Phone +33 388 732162 · Fax +33 388 730118
e-Mail sales@comtec-crystals.com
Internet: <http://www.comtec-crystals.com>
sous réserve de modifications.