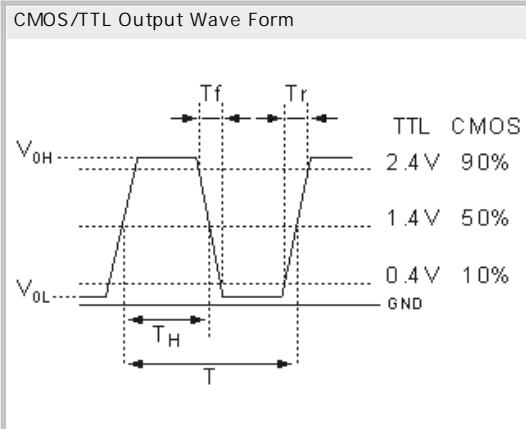
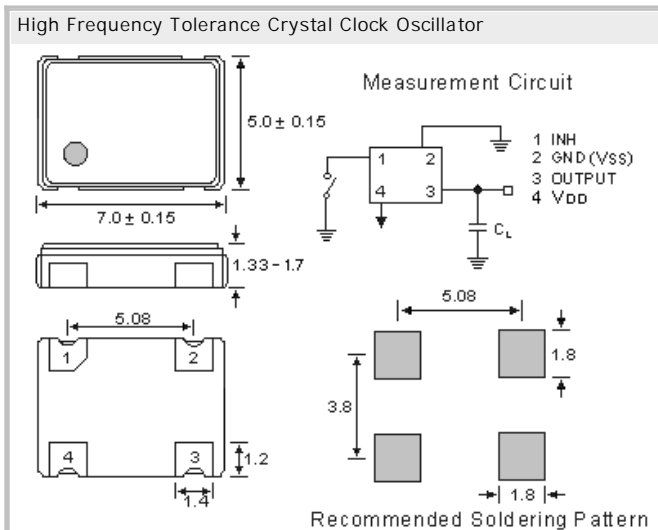


High Frequency Tolerance, Crystal Clock Oscillator
CXO7050TR3.3 3.3V, Phase Jitter 300fs

- SMD in ceramic case (7.0 x 5.0 x 1.8) mm
- Tri-State Enable / Disable
- LVCMOS Square Wave
- on Tape & Reel (Tape 16mm)
- RoHS conform; Lead-free product
- Vibration: MIL-STD-202F method 204, 35G, 50 to 2000 Hz
- Shock: MIL-STD-202F method 213B, test cond. E, 1000GG 1/2 sine wave
- Available in many standard and special frequencies



Specifications

Holder Type:	High Frequency Tolerance Crystal Clock Oscillator CXO7050TR3.3 Tri-State 3.3V (Voltage code is " 3.3 ") LVCMOS
Frequency Range:	1.0 MHz ~ 56.0MHz
Frequency Tolerance at 25°C:	± 2.0 ppm (max.)
Operating Temperature Range:	± 9.0 ppm over - 10°C to + 70°C; ± 15.0 ppm over - 40°C to + 85°C
Storage Temperature:	-55°C to +150°C
Power Supply Voltage (Vdd):	+ 3.3V D.C. ± 5%
Maximum Supply Current:	4.0mA max. (1.00MHz ~ 19.99MHz) 5.0mA max. (20.0MHz ~ 39.99MHz) 7.0mA max. (40.0MHz ~ 56.00MHz)
Output Load:	15pF
Output Symmetry (Duty Cycle):	50% ± 5% (measured at 50% Vdd)
Output Logic Levels:	High "1" Vdd-0.4(min) // Low "0" 0.1V (typical), 0.4V (max.)
Rise/Fall Time (10% to 90% Vdd):	2 ns (typical) < 4 ns max.
Tri-state Function on Pin 1:	Pin 1 = H or open.... Output active at pin 3 Pin 1 = L.... high impedance at pin 3
Phase Jitter (RMS) [25MHz, 3.3V]:	300fs (typical) [12kHz to 20MHz integrated]
SSB Phase Noise [25MHz, 3.3V]:	-65dBc/Hz @ 10Hz, -100dBc/Hz @ 100Hz, -130dBc/Hz @ 1kHz -145dBc/Hz @ 10kHz, -148dBc/Hz @ 100kHz, -152dBc/Hz @ 1MHz, -155dBc/Hz @ 10MHz
Aging:	< ± 2ppm max. / for the first year
Reflow Condition:	260°C max for 10 sec.

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