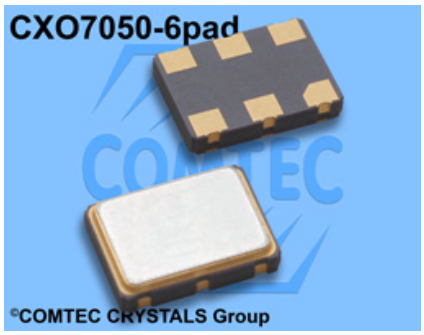
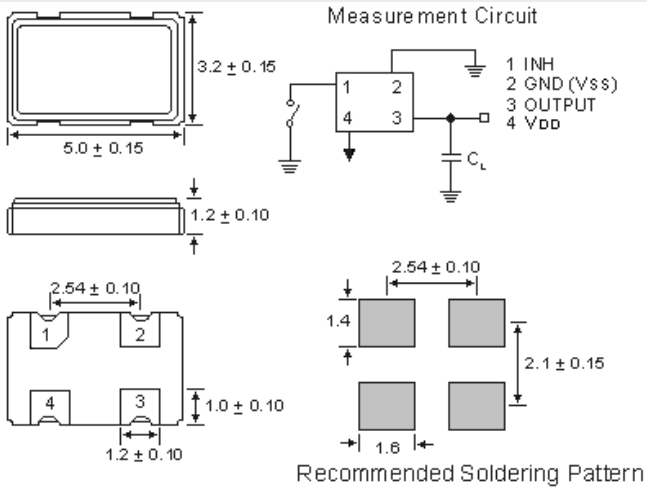


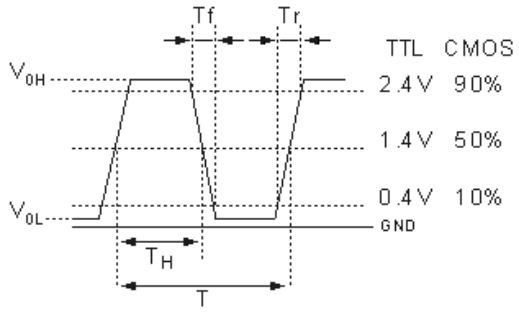
Programmable Crystal Clock Oscillator
CXP5032TM3.3, (3.3V 1 -200MHz LVCMOS)

- SMD in ceramic case (5.0 x3.2 x 1.2) mm
- Tri-State Enable / Disable
- LVCMOS Square Wave
- on Tape & Reel (Tape 16mm) or on trays
- RoHS conform; Lead-free product
- Vibration: MIL-STD-202F method 204, 35G, 50 to 2000 Hz
- Shock: MIL-STD-202F method 213B, test cond. E, 1000GG 1/2 sine wave
- Available in many standard and special frequencies

Programmable Crystal Clock OscillatorCXP5032TF3.3V



CMOS/TTL Output Wave Form



Specifications

Holder Type:	Programmable Crystal Clock Oscillator CX5032TM2.5 (± 2.5V LVCMOS)
Frequency Range:	3.3V (1.0 ~ 200.0MHz)
Frequency:	40.000000 MHz Blank, Ground Frequency
Frequency Stability at 25°C:	±25 ppm
Operating Temperature Range:	-40°C to +85°C, (Inclusive of 25°C tolerance, operating Temp., Supply Voltage, & Load, aging, shock and vibration)
Storage Temperature:	-55°C to +150°C
Power Supply Voltage (Vdd):	+ 2.5V D.C. ± 10%
Max. Supply Current 25/200MHz:	2.5V/3.0/13mA (15pF)
PLL Off: 25MHz Supply Current:	2.5V 2.5mA (15pF)
PLL On 200MHz Supply Current:	2.5V 12.5mA (15pF)
Rise/Fall Time:	3.0ns typ. (25MHz, PLL off) 1.5ns typ. (200MHz, PLL off)
Output Load:	15pF
Start Up Time:	5ms max.
Output Symmetry (Duty Cycle):	50% ±5% (measured at 50% Vdd)
Period Jitter, peak to peak:	70ps typical
Low Phase Noise (typical):	-114dBc/Hz at 1KHz offset (133MHz)
Tri-state Function Pin 1 (option):	Pin 1 = H or open.... Output active at pin 3 Pin 1 = L.... high impedance at pin 3
Output Disable/Enable Time:	150 ns
Aging:	< ±5ppm max. / year (max.)
Reflow Condition:	260°C max. for 10 sec.

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