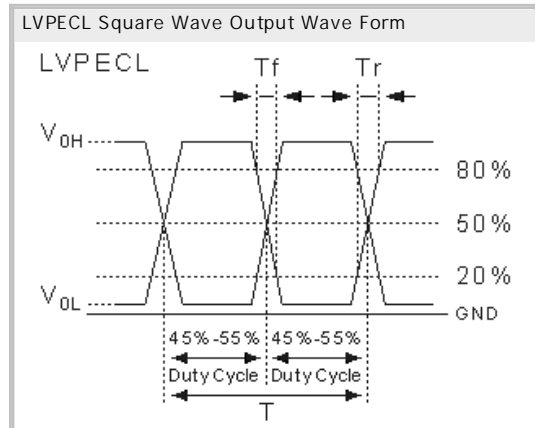
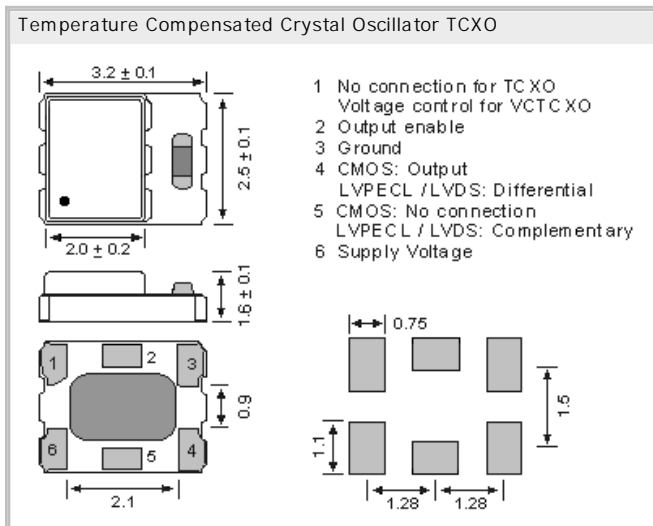


Temperature Compensated Crystal Oscillator TCXO
Type TCXO3225PQN3.3 LVPECL 3.3V

- SMD in ceramic case (3.2 x 2.5 x 1.6) mm
- Tri-State Enable / Disable
- LVPECL Differential
- on Tape & Reel
- RoHS conform; Lead-free product
- Vibration: MIL-STD-202F method 204, 35G, 50 to 2000 Hz
- Shock: MIL-STD-202F method 213B, test cond. E, 1000GG 1/2 sine wave
- Available in many standard and special frequencies



Specifications

Holder Type:	Temperature Compensated Crystal Oscillator TCXO, Type TCXO3225PQN3.3 LVPECL 3.3V
Frequency Range:	10.0 MHz ~ 1.450 MHz
Initial Calibration Tolerance:	± 1.0 ppm max. at 25°C ± 2°C
Frequency Stability Codes (FSC):	
(FSC) Temperature (ref to +25°C):	± 0.5 ppm; ± 2.5 ppm over -30+85°C; ± 1.0 ppm over -40+85°C (Inclusive Operating Temp., Supply Voltage, & Load)
(FSC) Aging:	± 1.0 ppm max. / year (max.)
(FSC) Voltage Change:	± 0.2 ppm max. , for a ±5% input voltage change.
(FSC) Load Change:	± 0.2 ppm max. , for a ±10% load condition change.
Power Supply Voltage (Vdd):	+ 3.3V D.C. ± 5% (voltage code " 3.3 ")
Output Symmetry (Duty Cycle):	45/55%
Start Up Time:	5 ms max.
Storage Temperature:	-55°C to +150°C
Maximum Supply Current:	156MHz : 22 mA ; 600MHz : 30 mA ; 800MHz : 32 mA ; 1 GHz : 36 mA
Current with Output Disabled:	18 mA (typical)
Output Load:	Differential
Integrated Phase Jitter:	0.8 pS typical (12 KHz to 20 MHz) ; < 400 fS (1.875 KHz to 20 MHz)
SSB Phase Noise (Frequency 125.0MHz):	-51dBc/Hz @ 10Hz, -93dBc/Hz @ 100Hz, -111dBc/Hz @ 1kHz -123dBc/Hz @ 10kHz, -125dBc/Hz @ 100kHz, -135dBc/Hz @ 1MHz, -155dBc/Hz @ 10MHz
Output Voltage (VOH) (VOL):	Vdd - 1.03 (min.) , Vdd - 0.6 (max.) / Vdd - 1.03 (min.) , Vdd - 0.6 (max.)
Output Enable Funktion on pad 2:	Output Enable Time/ Disable Time // 200nS max. / 50nS max.
Rise/Fall Time (20% to 80% Vdd):	0.2 ns typical < 0.5 ns max.

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