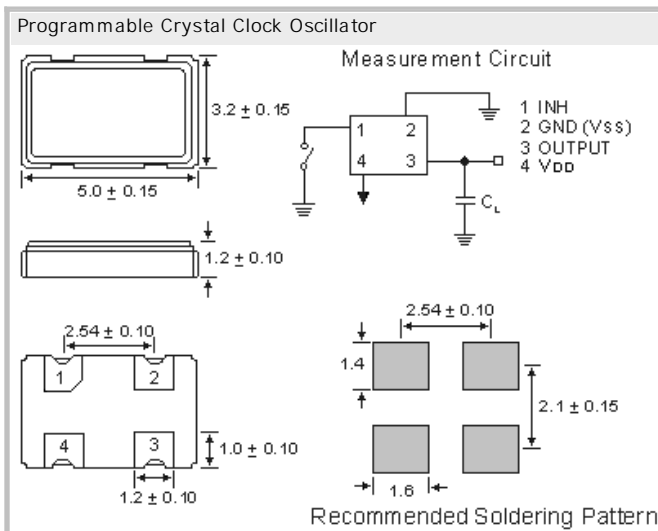
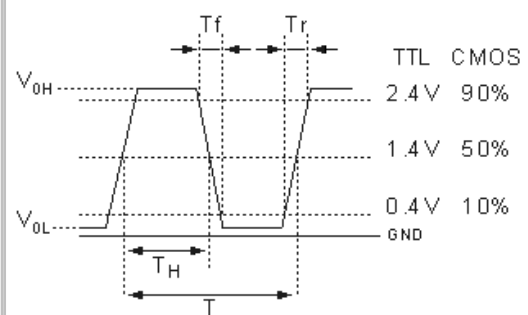


Programmable Crystal Clock Oscillator
5.0V 1-133MHz, 3.3V 1-100MHz, 2.7V 1-66MHz

- Short lead time. From one day to one week
- Ceramic case (5.0 x 3.2 x 1.2) mm
- CMOS/TTL Square Wave
- Available in many frequencies
- packed in antistatic tubes
- RoHS conform; Lead-free product



CMOS/TTL Output Wave Form



Specifications

Holder Type:	Programmable Crystal Clock Oscillator CXP5032TC5.0 (± 5.0V CMOS), CXP5032TC3.3 (± 3.3V CMOS), CXP5032TC2.7 (± 2.7V CMOS)
Frequency Range:	5.0V 1-133MHz, 3.3V 1-100MHz, 2.7V 1-66MHz
Frequency Stability at 25°C:	± 25 ppm / ± 50 ppm / ± 100 ppm
Operating Temperature Range:	-40°C to +85°C (Inclusive of 25°C tolerance, operating temperature range, ± 10% input voltage variation, load change, aging shock and vibration)
Storage Temperature:	-55°C to +125°C
Power Supply Voltage (V _{DD}):	+ 5.5V max. to 2.7V min.
Maximum Supply Current:	4.5-5.5V V _{DD} -> 45mA max., 3.0-3.6V V _{DD} -> 25mA max., 2.5-3.0V V _{DD} -> 20mA max.
Standby Current:	10µA typical, 50µA max.
Load Capacitance (CL):	CL for TTL levels: 4.5V -5.5V V _{DD} 40 MHz 50pF; 40 - 133 MHz 25pF CL for CMOS levels: 4.5V -5.5V V _{DD} 60 MHz 50pF; 66 - 133 MHz 25pF 3.0V -3.6V V _{DD} 40 MHz 30pF; 40-100 MHz 15pF; 2.5V -3.0V V _{DD} 66 MHz
Output Symmetry (Duty Cycle):	40/60% or 45/55%
Output Voltage (V _{OH}) (V _{OL}):	90% of V _{DD} min. / 10% of V _{DD} max.
Rise/Fall Time (10% to 90% V _{DD}):	4 ns max. < 2.0 ns typical
Start Up Time:	10 ms max.
Tri-state (Option):	Pin 1 = H or open.... Output active at pin 3 Pin 1 = L.... high impedance at pin 3
Output Enable Time:	100 ns
RMS Jitter:	± 50ps max.
Aging:	< ± 5ppm max. / year (max.)

GERMANY:

COMTEC CRYSTALS GmbH · Sultenstrasse 12-14
8 5 5 8 6 P o i n g / G E R M A N Y
Phone +49 8121 778160 · Fax +49 8121 778177
e-Mail info@comtec-crystals.com
Internet: <http://www.comtec-crystals.com>
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FRANCE:

COMTEC CRYSTALS SARL · 23, rue du Faucon
6 7 5 0 0 H a g u e n a u / F R A N C E
Phone +33 388 732162 · Fax +33 388 730118
e-Mail sales@comtec-crystals.com
Internet: <http://www.comtec-crystals.com>
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