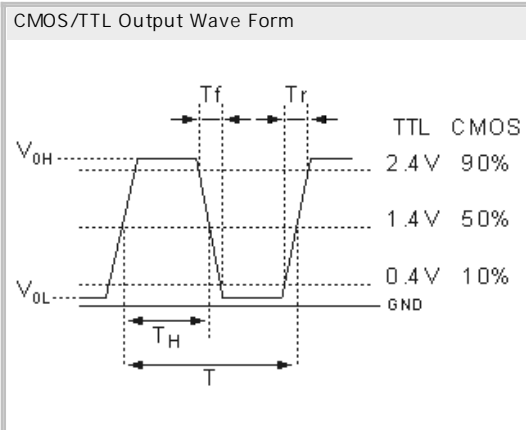
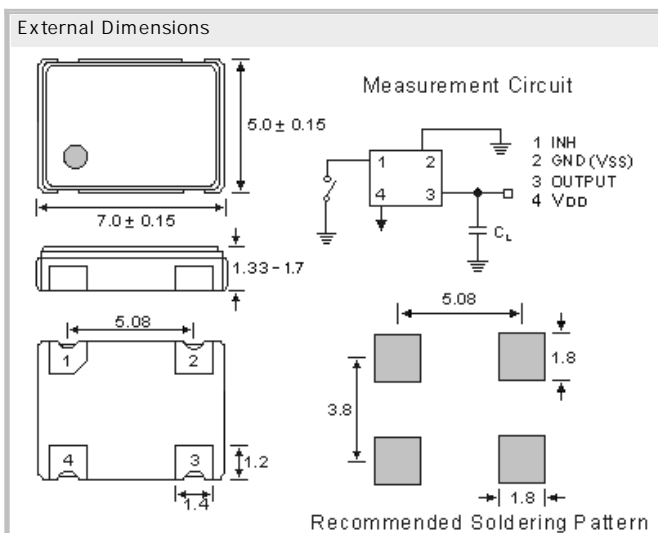


Crystal Clock Oscillator CXO7050TA1.8 1.8V 65µA

- SMD in ceramic case (7.0 x 5.0 x 1.7) mm
- Tri-State Enable / Disable
- CMOS/TTL Square Wave
- on Tape & Reel (Tape 16mm)
- RoHS conform; Lead-free product
- Vibration: MIL-STD-202F method 204, 35G, 50 to 2000 Hz
- Shock: MIL-STD-202F method 213B, test cond. E, 1000GG 1/2 sine wave
- Available in many standard and special frequencies



Specifications

| | |
|----------------------------------|--|
| Holder Type: | CXO7050TA1.8 Tri-State 1.8V (Voltage code is " 1.8 ") |
| Frequency Range: | 10.0kHz ~ 100.0kHz |
| Frequency Stability at 25°C: | ± 20 to ± 100 ppm |
| Operating Temperature Range: | -20°C to +70°C (min. ± 15ppm), -40°C to +85°C (min. ± 25ppm) (Inclusive Operating Temp., Supply Voltage, & Load) |
| Storage Temperature: | -55°C to +125°C |
| Power Supply Voltage (Vdd): | + 1.8V D.C. ± 10% |
| Maximum Supply Current: | 75µA typical; 100µA max. |
| Output Load: | 15pF CMOS |
| Output Symmetry (Duty Cycle): | 50% ± 3% typical; 50% ± 5% max. (measured at 50% Vdd) |
| Output Voltage (VOH) (VOL): | 90% of Vdd min. / 10% of Vdd max. |
| Rise/Fall Time (10% to 90% Vdd): | 10 ns max. < 5 ns typical |
| Start Up Time: | 0.8 ms max. < 5 ms typical |
| Tri-state Function Pin 1: | Pin 1 = H or open... Output active at pin 3 Pin 1 = L... high impedance at pin 3 |
| Aging: | < ± 3ppm max. / year (max.) |
| Reflow Condition: | 260°C max. for 10 sec. |

GERMANY:

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