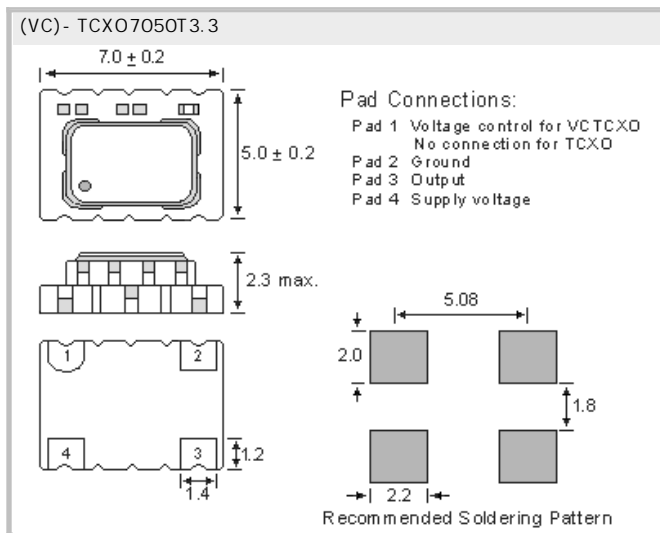
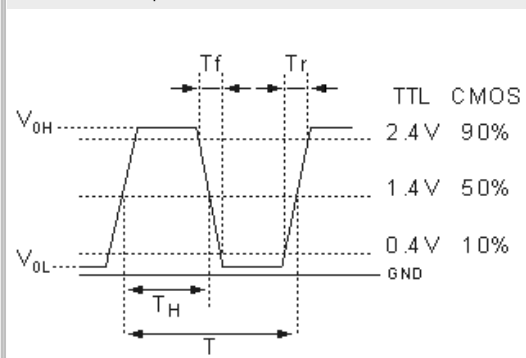


(VC)-TCXO-Oscillator SMD (VC)- TCXO7050T3.3
3.3V CMOS kHz range

- Voltage Control Temperature Compensated Crystal Oscillator
- SMD in ceramic case (7.0 x 5.0 x 2.3) mm
- CMOS Square Wave Output;
Wave form code is " T "
- RoHS conform; Lead-free product; Tape & Reel
- Vibration: MIL-STD-202F method 204,
35G, 50 to 2000 Hz
- Shock: MIL-STD-202F method 213B,
test cond. E, 1000GG 1/2 sine wave
- Available in many standard and special frequencies



TTL/CMOS Output Wave Form



Specifications - Product No. G000032768QPZUPN1ANB

Holder Type:	(VC)-TCXO7050T3.3 3.3V (Voltage code is " 3.3 ")
Frequency:	32.768000 kHz
Input Voltage:	+ 3.3V ± 10%
Frequency Stability:	± 0.5 ppm at 25°C
Freq. Stability vs Temperature:	± 0.5 ppm from ±0°C to +50°C
Freq. Stability vs Aging:	± 1.0 ppm, first year at 25 °C
Freq. Stability vs Voltage Change:	± 0.3 ppm, for a ± 5% input voltage change
Freq. Stability vs Load Change:	± 0.3 ppm, for a ± 10% load condition change
Operating Temperature Range:	±0°C to +50°C
Maximum Supply Current:	12.0 mA at +3.3V
Output Load:	15pF
Output Logic Levels:	Logic High "1" : 90% Vdd min. (HCMOS) Logic Low "1" : 10% Vdd min. (HCMOS)
Output Symmetry (Duty Cycle):	50% ± 5%
Pin 1 Options VCTCXO only:	Control Voltage: Standard +1.5V ± 1.0V for all input voltages. Slope Polarity: Positive slope. Positive voltage for positive frequency shift. Input Impedance: 10kOhm min.; Linearity ±10% max. Modulation Bandwidth (at-3 dB) : 3kHz min.
Pin 1 Frequency Deviation Range:	± 5.0 ppm (min.) with Vcon = +1.5V ± 1.0V
Start Up Time:	10 ms (typ.); 5ms (max.) (reach90% amplitude and at +25°C ± 2°C
Rise/Fall Time CMOS:	10.0ns max. Measured between 20% and 80% Vdd) of the wave form (CL = 15pF)
Storage Temperature:	-40 to +85°C, -55 to +125°C
Reflow Condition:	260°C max. for 10 sec.

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