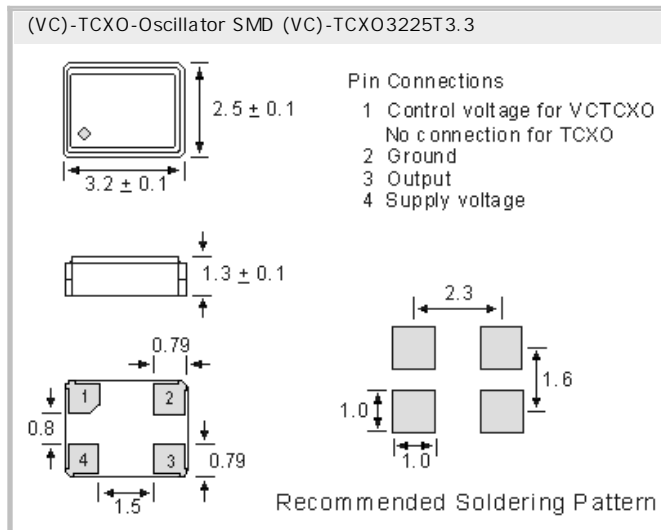
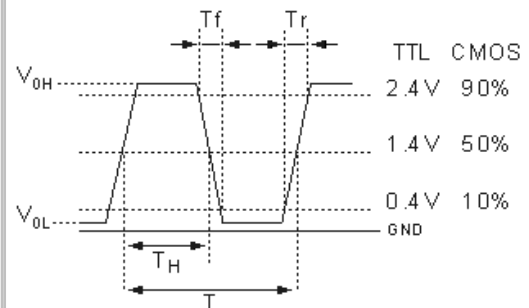


(VC)-TCXO-Oscillator SMD
 (VC)-TCXO3225T3.3 3.3V CMOS/TTL

- Temperature Compensated Crystal Oscillators
- SMD in ceramic case (3.2 x 2.5 x 1.0) mm
- CMOS/TTL Square Wave Output;
Wave form code is " T "
- on Tape & Reel
- RoHS conform; Lead-free product



CMOS/TTL Output Wave Form



Specifications - Product No. G010000000ESXUPN43LA

Holder Type:	(VC)-TCXO3225T3.3 3.3V (Voltage code is " 3.3 ")
Frequency:	10.000000 MHz
Frequency Stability:	± 2.0 ppm at 25°C
Freq. Stability vs Temperature:	± 2.0 ppm from -40°C to +85°C
Input Voltage:	+ 3.3V $\pm 10\%$
Freq. Stability vs Aging:	± 1.0 ppm, first year at 25 °C
Freq. Stability vs Voltage Change:	± 0.3 ppm, for a $\pm 5\%$ input voltage change
Freq. Stability vs Load Change:	± 0.3 ppm, for a $\pm 10\%$ load condition change
Operating Temperature Range:	-40°C to +85°C
Maximum Supply Current:	6.0 mA max.
Output Load:	15pF
Voltage Control on pin 1 (EFC):	Electronic Frequency Tuning (EFC) Optionally
Pin 1 Options VCTCXO only (EFC):	Control Voltage: Standard +1.5V ± 1.0 V for all input voltages. Slope Polarity: Positive slope. Positive voltage for positive frequency shift. Input Impedance: 50kOhm min.; Linearity $\pm 10\%$ max. Modulation Bandwidth (at-3 dB) : 20kHz min.
Frequency Deviation Range (EFC):	± 5.0 ppm (min.) with $V_{con} = +1.5V \pm 1.0V$ (Optionally)
Output Logic Levels:	Logic High "1" : 90% V_{dd} min. // Logic Low "0" : 10% V_{dd} min.
Output Symmetry (Duty Cycle):	40/60% ; Measured at 50% V_{dd}
Rise/Fall Time CMOS:	10.0ns (max.) Measured between 20% and 80% V_{dd} of the wave form (CL = 15pF)
Start Up Time:	10 ms (max.); 5ms (typ.)
Phase Noise (10.00MHz 3.3V):	- 96dBc/Hz at 10Hz offset -122dBc/Hz at 100Hz offset -138dBc/Hz at 1kHz offset -145dBc/Hz at 10kHz offset -150dBc/Hz at 100kHz offset
Storage Temperature:	-55 to +105°C
Reflow Condition:	260°C max. for 10 sec.

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