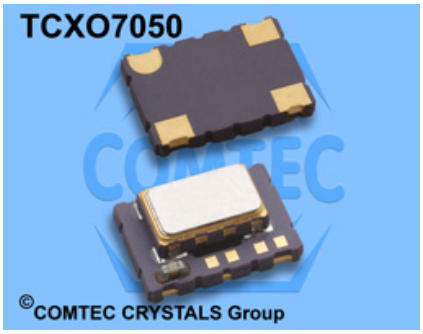
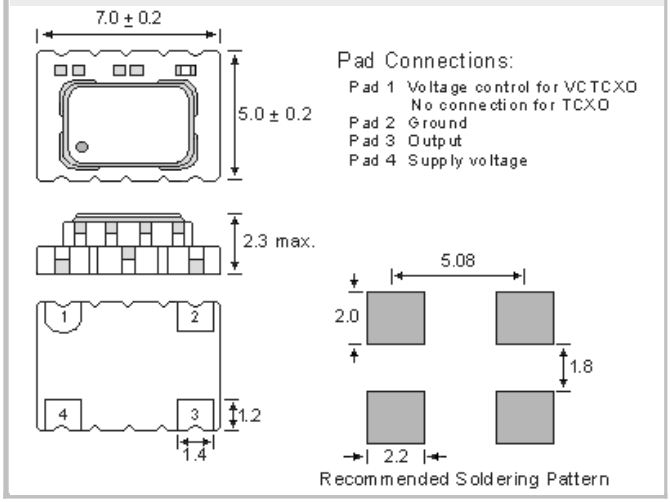


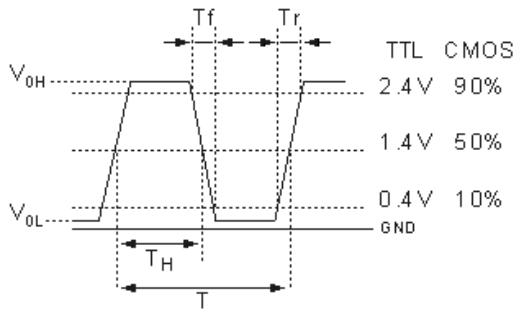
(VC)-TCXO-Oscillator SMD
(VC)-TCXO7050TL3.3 3.3V CMOS/TTL

- Temperature Compensated Crystal Oscillators
- SMD in ceramic case (7.0 x 5.0 x 2.3) mm
- CMOS/TTL Square Wave Output; Wave form code is " T "
- on Tape & Reel
- RoHS conform; Lead-free product

(VC)-TCXO-Oscillator SMD (VC)-TCXO7050T3.3



CMOS/TTL Output Wave Form



Specifications - Product No. G050000000CJZUPN1ANB

Holder Type:	(VC)-TCXO-Oscillator SMD (VC)-TCXO7050TL3.3 3.3V CMOS/TTL
Frequency:	50.000000 MHz
Input Voltage:	+ 3.3V ± 10%
Frequency Stability:	± 0.5 ppm at 25°C
Freq. Stability vs Temperature:	± 0.5 ppm from ±0°C to +50°C of 25°C tolerance, operating Temp., Supply Voltage, & Load, aging, shock and vibration)
Freq. Stability vs Aging:	± 1.0 ppm, first year at 25°C
Freq. Stability vs Voltage Change:	± 0.3 ppm, for a ± 5% input voltage change
Freq. Stability vs Load Change:	± 0.3 ppm, for a ± 10% load condition change
Operating Temperature Range:	±0°C to +50°C
Maximum Supply Current:	14.0 mA max.
Output Load:	15pF
Output Logic Levels:	Logic High "1" : 90% Vdd min. // Logic Low "0" : 10% Vdd min.
Voltage Control on pin 1 (EFC):	Electronic Frequency Tuning (EFC) Optionally
Pin 1 Options VCTCXO only (EFC):	Control Voltage: Standard +1.5V ± 1.0V for all input voltages. Slope Polarity: Positive slope. Positive voltage for positive frequency shift. Input Impedance: 50kOhm min.; Linearity ±10% max. Modulation Bandwidth (at-3 dB) : 20kHz min.
Frequency Deviation Range (EFC):	± 5.0ppm (min.) with Vcon = +1.5V ±1.0V (Optionally)
Output Symmetry (Duty Cycle):	45/55% ; Measured at 50% Vdd
Rise/Fall Time CMOS:	10.0ns (max.) Measured between 20% and 80% Vdd of the wave form (CL = 15pF)
Start Up Time:	10 ms (max.), 5ms (typ.)
Phase Noise (10.00MHz 3.3V):	- 96dBc/Hz at 10Hz offset -122dBc/Hz at 100Hz offset -138dBc/Hz at 1kHz offset -145dBc/Hz at 10kHz offset -150dBc/Hz at 100kHz offset
Storage Temperature:	-55 to +105°C
Reflow Condition:	260°C max. for 10 sec.

GERMANY:
COMTEC CRYSTALS GmbH · Sultenstrasse 12-14
85586 Poing / GERMANY
Phone +49 8121 778160 · Fax +49 8121 778177
e-Mail info@comtec-crystals.com
Internet: http://www.comtec-crystals.com
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FRANCE:
COMTEC CRYSTALS SARL · 23, rue du Faucon
67500 Haguenau / FRANCE
Phone +33 388 732162 · Fax +33 388 730118
e-Mail sales@comtec-crystals.com
Internet: http://www.comtec-crystals.com
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