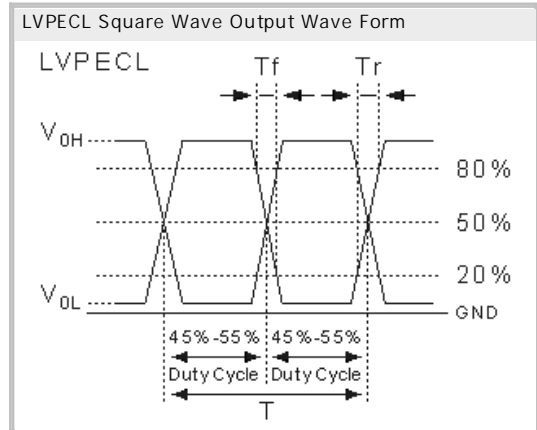
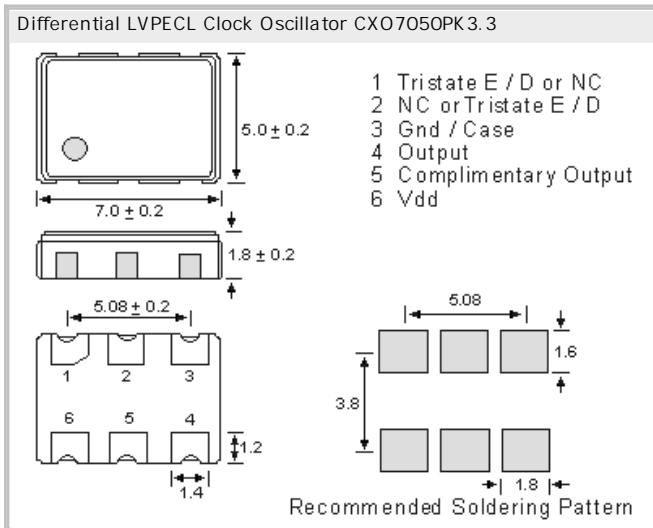


Differential LVPECL Clock Oscillator  
CXO7050PK3.3, 3.3V, 0.2pS Jitter, non PLL

- SMD in ceramic case (7.0 x 5.0 x 1.8) mm
- Tri-State Enable / Disable on pad No. 1
- Femto second integrated phase jitter (200 fs typical, 12 KHz to 20 MHz)
- Superior phase noise (-135 dBc/Hz at 10 KHz and -142 dBc/Hz at 100 KHz offset)
- RoHS conform; Lead-free product; on Tape (16mm) & Reel
- Vibration: MIL-STD-202F method 204, 35G, 50 to 2000 Hz
- Shock: MIL-STD-202F method 213B, test cond. E, 1000GG 1/2 sine wave
- High performance with surprisingly low price



## Specifications - Product No. G166670000DVCUPB90BB

Holder Type:	CXO7050PK3.3; 3.3V(Voltage code is "3.3"); Tri-State on pad 1
Frequency:	166.670000 MHz
Frequency Stability at 25°C:	± 50.0 ppm
Operating Temperature Range:	± 50.0 ppm over -10°C to +70°C (inclusive of 25°C tolerance, ±10% input voltage variation, load change, aging, shock and vibration)
Storage Temperature:	-55°C to +150°C
Power Supply Voltage (Vdd):	+ 3.3V D.C. ± 5%
Maximum Supply Current (15pF load):	30.0 mA typ.
Output Swing:	595 mV min; 750 mV typical; 930 mV max.
Output Logic Levels:	High "1" $V_{oh}$ $V_{dd}-1.025V$ min., $V_{dd}-0.95V$ typical; $V_{dd}-0.88V$ max. Condition: $R_L=50\Omega$ to ( $V_{dd}-2.0V$ ) Low "0" $V_{ol}$ $V_{dd}-1.810V$ min., $V_{dd}-1.70V$ typical; $V_{dd}-1.62V$ max. Condition: $R_L=50\Omega$ to ( $V_{dd}-2.0V$ )
Output Symmetry (Duty Cycle):	50% ± 5% max. measured at 50% waveform
Load:	$R_L=50\Omega$ into ( $V_{dd}-2.0V$ ) or Thevenin equivalent (terminating resistors required on all outputs).
Rise/Fall Time:	0.5ns typical, 0.5ns max. @ 20% to 80% of PECL wave form
Start Up Time:	5.0 ms typical; 10 ms max.
OE Function Pin 1:	Enable    When 70% min. of VDD to Enable Output. Enable time : 10 ms max. Disable    When 30% max. of VDD to Disable Output. Disable current : 10 $\mu A$ max. , Disable time : 0.2 $\mu s$ max.
Phase Jitter (12 kHz to 20 MHz):	0.2 ps typical, 0.5 ps (max.), for 156.250 MHz, 3.3V
Phase Noise (156.25 MHz):	-50dBc/Hz @ 10Hz, -80dBc/Hz @ 100Hz, -115dBc/Hz @ 1kHz -135dBc/Hz @ 10kHz, -142dBc/Hz @ 100kHz, -147dBc/Hz @ 1MHz, -152dBc/Hz @ 10MHz
Aging:	< ± 3ppm max. for the first year
Reflow Condition:	260°C max for 10 sec.

### GERMANY:

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