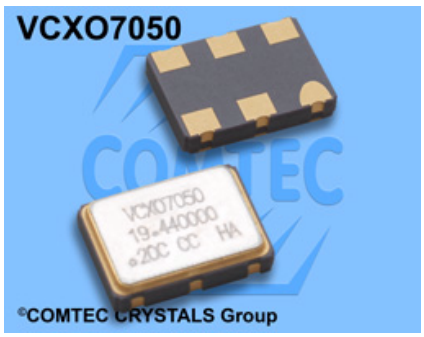
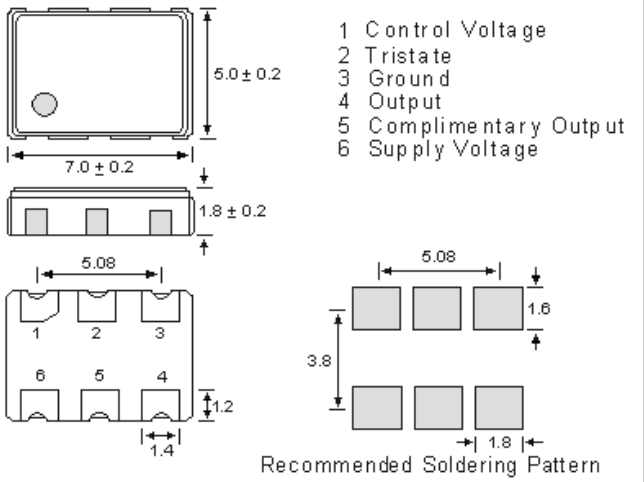
COMTEC CRYSTALS Group — Data Sheet



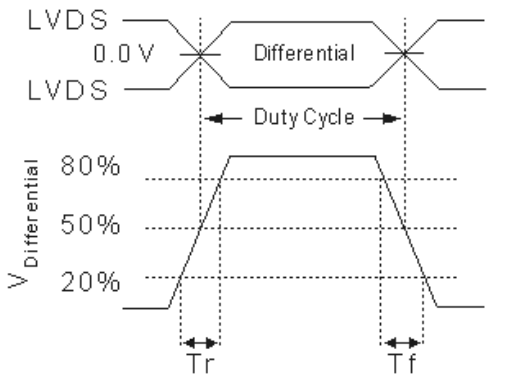
VCXO LVDS Differential Oscillator
VCXO7050DW3.3 3.3V

- SMD in ceramic case (7.0 x 5.0 x 1.8) mm
- LVDS Square Wave Output Wave Form
- High Q fundamental crystal
  + multiplier circuit
- RoHS conform; Lead-free product
- Vibration: MIL-STD-202F method 204, 35G, 50 to 2000 Hz
- Shock: MIL-STD-202F method 213B, test cond. E, 1000GG 1/2 sine wave
- Available in many standard and special frequencies

VCXO LVDS Differential Oscillator



LVDS Square Wave Output Wave Form



Specifications - Product No. G200000000ACUUPC43BB

Holder Type:	VCXO LVDS Differential Oscillator VCXO7050DW3.3 3.3V(Voltage code is "3.3"); Tri-State on pad 2
Frequency:	200.000000 MHz
Frequency Stability at 25°C:	± 50.0 ppm
Operating Temperature Range:	± 50.0 ppm over -40°C to +85°C (incusive of 25°C tolerance, ± 10% input voltage variation, load change, aging, shock and vibration)
Frequency Deviation:	±100ppm
Storage Temperature:	-55°C to +150°C
Power Supply Voltage (Vdd):	+ 3.3V D.C. ± 5%
Maximum Supply Current (15pF load):	100 mA
Load:	RL=50 Ω for (Vdd-2.0V)
Output Logic Levels:	High "1" Voh Vdd-1.025min., -0.95typ., -0.88max. ; RL= 50 Ω to (Vdd-2.0V) Low "0" VoL Vdd-1.810min., -1.70typ., -1.62max.; RL= 50 Ω to (Vdd-2.0V)
Output Symmetry (Duty Cycle):	50% ± 5%max.@ Vdd-1.3V
Voltage Control:	1.65V DC Center / 0.3V to 3.0V Range
Rise/Fall Time:	1.5ns max. @ 20% to 80% of PECL wave form
Start Up Time:	10 ms max.
Tri-state Function Pin 2:	When Pin 2 = 1, Output Enable When Pin 2 (at 0.0V), Output High impedance, Disable current: 50µA max.
Phase Jitter (12 kHz to 20 MHz):	2.6 ps typ., 4ps max., for 155.520MHz
Phase Noise (155.520 MHz):	-60dBc/Hz @ 10Hz, -90dBc/Hz @ 100Hz, -115dBc/Hz @ 1kHz -125dBc/Hz @ 10kHz, -119dBc/Hz @ 100kHz, -120dBc/Hz @ 1MHz
Aging:	< ±3ppm max. for the first year
Input Impedance:	2 MΩ (min.)
Reflow Condition:	260°C max for 10 sec.

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