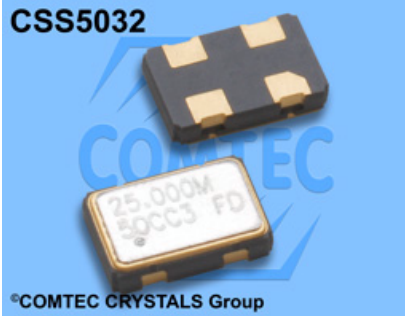
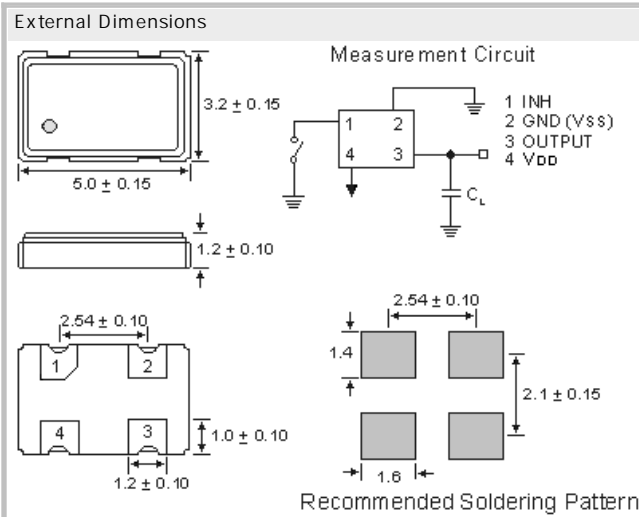


CSS5032

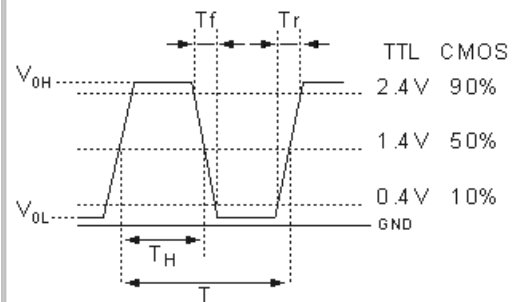


EMI Reducing Spread Spectrum Clock Oscillator CSS5032TR3.3 3.3V

- SMD in ceramic case (5.0 x 3.2 x 1.2) mm
- Tri-State Enable / Disable
- CMOS/TTL Square Wave
- on Tape & Reel (Tape 16mm, 1000pcs per reel)
- RoHS conform; Lead-free product
- Vibration: MIL-STD-202F method 204, 35G, 50 to 2000 Hz
- Available in many standard and special frequencies



CMOS/TTL Output Wave Form



Specifications

Holder Type:	CSS5032TR3.3 Tri-State 3.3V (Voltage code is " 3.3 ")
Frequency Range:	6.000 MHz to 160.000 MHz
Frequency Stability at 25°C:	±20 to ±100 ppm
Operating Temperature Range:	-20°C to +70°C, -40°C to +85°C (Inclusive Operating Temp., Supply Voltage, & Load)
Storage Temperature:	-65°C to +150°C
Spread Type (%):	Total % Down Spread (D) Center Spread (C) -1% -1% (D1) ±0.5 (C0.5) -3% -3% (D3) ±1.5 (C1.5)
EMI Reduction:	-9 dBc min. 100 MHz at C0.5, -15 dBc min. 100MHz at C1.5 With respect to the dB level when no modulation. EMI reduction applies to the whole spectrum.
Power Supply Voltage (Vdd):	+ 3.3V D.C. ± 5 %
Maximum Supply Current:	10mA typ. (10.000MHz ~ 50.000MHz), 18mA typ. (50.000MHz ~ 100.000MHz), 35mA typ. (100.000MHz ~ 160.000MHz)
Modulation Carrier Freq. (Dither rate):	6.9 kHz min.; 55.5kHz max. Frequency dependent
Output Load:	15pF CMOS
Output Symmetry (Duty Cycle):	50% ± 5 % CL= 15pF; 50% Vdd
Output Voltage (VOH) (VOL):	2.4V min; 3.2V typical 90% of Vdd min. 0.5V max; 0.2V typical 10% of Vdd max.
Rise/Fall Time (10% to 90% Vdd):	4 ns max.
Start Up Time:	2 ms typical; 5 ms max.
Cycle-to-Cycle Jitter:	±250 ps typical; ± 300 ps max.
Tri-state Function Pin 1:	Pin 1 = H or open... Output active at pin 3 Pin 1 = L... high impedance at pin 3; Output enable/disable time: 100ns max.
Aging:	< ±5ppm max. / year (max.)
Reflow Condition:	260°C max for 10 sec.
See also ...:	http://www.comtec-crystals.com/docs/G/GFH2.htm

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