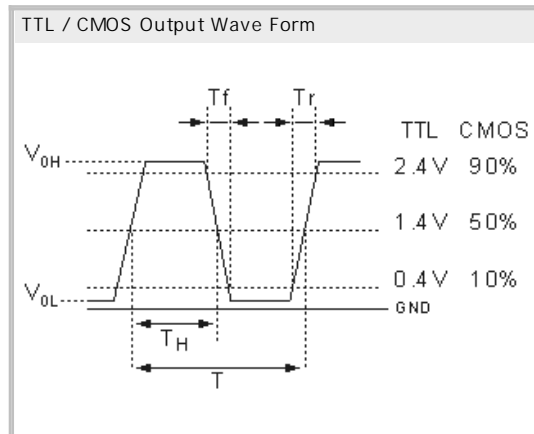
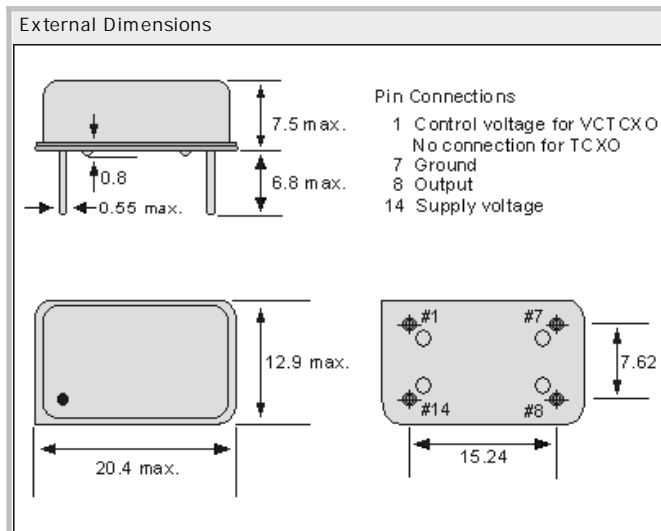


VCTCXO-Oscillator DIL14 VCTCXO-14T3.3 3.3V

- Voltage Control Temperature Compensated Crystal Oscillator, DIL 14 in metall case
- Electrical Frequency Tuning (EFC) by external Control voltage
- TTL / CMOS Square Wave Output; Wave form code is " T "
- RoHS conform; Lead-free product
- Vibration: MIL-STD-202F method 204, 35G, 50 to 2000 Hz
- Shock: MIL-STD-202F method 213B, test cond. E, 1000GG 1/2 sine wave
- Available in many standard and special frequencies



Specifications

Holder Type:	VCTCXO-14T3.3 3.3V (Voltage code is " 3.3 ")
Frequency Range:	1.25 MHz to 156.0 MHz
Standard Frequencies (Part list):	10.0, 12.8, 13.0, 14.4, 15.36, 16.384, 19.2, 19.44, 19.68, 25.0, 20.0, 27.0, 38.88, 40.0, 77.76, 125.0, 155.52 MHz
Frequency Stability:	± 0.5ppm to ± 3.0ppm
Freq. Stability vs Temperature:	0° to 50° C (± 0.5ppm) to -40 to +85° C (± 1.0 to ± 3.0ppm)
Freq. Stability vs Aging:	± 1.0 ppm, first year at 25 °C
Freq. Stability vs Voltage Change:	± 0.3 ppm, for a ± 5% input voltage change
Freq. Stability vs Load Change:	± 0.3 ppm, for a ± 10% load condition change
Current Consumption:	2mA @ 8.192 MHz, 4mA @ 10.000 MHz 17mA @ 77.760 MHz, 35mA @ 155.520 MHz (Freq. dependent)
Output Load:	15pF
Output Logic Levels:	Logic High "1": 2.4Vmin. (TTL), 90% V _{dd} min. (HCMOS) Logic Low "1": 0.4Vmin. (TTL), 10% V _{dd} min. (HCMOS)
Output Symmetry (Duty Cycle):	40/60% normal, 45/55% tight at 50%
Pin 1 Options VCTCXO only:	Control Voltage: Standard + 1.5V ± 1.0V for all input voltages. Slope Polarity: Positive slope. Positive voltage for positive frequency shift. Input Impedance: 50kOhm min.; Linearity ± 10% max. Modulation Bandwidth (at-3 dB) : 20kHz min.
Rise/Fall Time (10% to 90% V _{dd}):	10.0 nsec. max., 20% to 80% of the wave form
Phase Noise (typical):	-110dBc/Hz at 100Hz -125dBc/Hz at 1kHz -132dBc/Hz at 10kHz -125dBc/Hz at 100kHz
Start Up Time:	10 ms (max.), 5ms (typ.)
Storage Temperature:	-55 to + 125° C
Reflow Condition:	260° C max. for 10 sec.

GERMANY:

COMTEC CRYSTALS GmbH · Sultenstrasse 12-14
8 5 5 8 6 P o i n g / G E R M A N Y
Phone +49 8121 778160 · Fax +49 8121 778177
e-Mail info@comtec-crystals.com
Internet: <http://www.comtec-crystals.com>
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FRANCE:

COMTEC CRYSTALS SARL · 23, rue du Faucon
6 7 5 0 0 H a g u e n a u / F R A N C E
Phone +33 388 732162 · Fax +33 388 730118
e-Mail sales@comtec-crystals.com
Internet: <http://www.comtec-crystals.com>
Sous réserve de modifications.