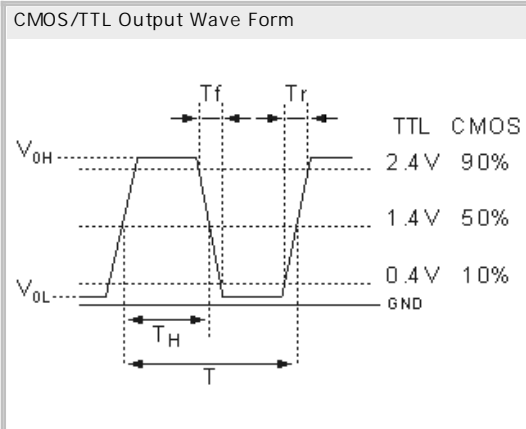
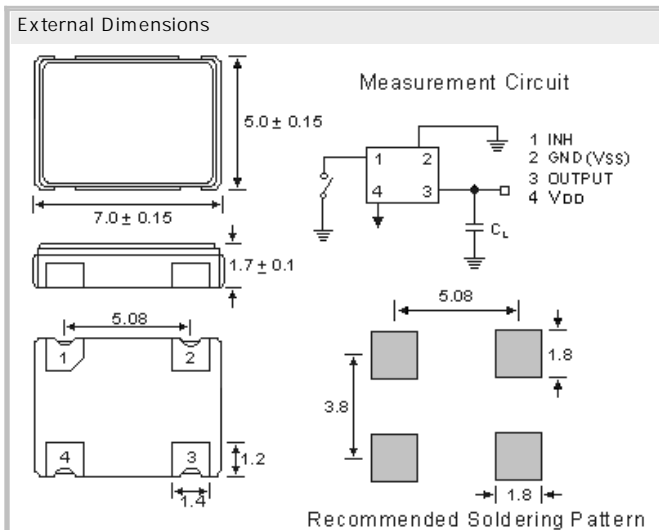


Programmable Crystal Clock Oscillator CXP7050TM
3.3V 1 - 200MHz, 2.5V 1 - 166MHz, 1.8V 1 - 110MHz

- SMD in ceramic case (7.0 x 5.0 x 1.7) mm
- Tri-State Enable / Disable
- CMOS/TTL Square Wave
- on Tape & Reel (Tape 16mm) or on trays
- RoHS conform; Lead-free product
- Vibration: MIL-STD-202F method 204, 35G, 50 to 2000 Hz
- Shock: MIL-STD-202F method 213B, test cond. E, 1000GG 1/2 sine wave
- Available in many standard and special frequencies



Specifications

Holder Type:	CXP7050TM3 (± 3.3V CMOS), CXP7050TM25 (± 2.5V CMOS), CXP7050TM18 (± 1.8V CMOS)
Frequency Range:	3.3V (1.0 ~ 200.0MHz), 2.5V (1.0 ~ 166.0MHz), 1.8V (1.0 ~ 110.0MHz)
Frequency:	24.000000 MHz Blank, Ground Frequency
Frequency Stability at 25°C:	± 25 ppm / ± 50 ppm / ± 100 ppm
Operating Temperature Range:	-40°C to +85°C, ± 25 ppm / ± 50 ppm / ± 100 ppm (Inclusive of 25°C tolerance, operating Temp., Supply Voltage, & Load, aging, shock and vibration)
Storage Temperature:	-55°C to +150°C
Power Supply Voltage (Vdd):	+ 3.3V D.C. ± 10%, + 2.5V D.C. ± 10%, + 1.8V D.C. ± 10%,
Max. Supply Current 25/200MHz:	3.3V/4.0/20mA; 2.5V/3.0/13.0mA; 1.8V/2.5/8.0mA; (15pF)
PLL Off: 25MHz Supply Current:	3.3V 4.0mA; 2.5V 2.5mA; 1.8V 2.0mA; (15pF)
PLL On 200MHz Supply Current:	3.3V 20mA; 2.5V 12.5mA; 1.8V 8.5mA; (15pF)
Rise/Fall Time:	3.3V 3.0ns typ. 2.5V 3.0ns typ. 1.8V 4.0ns typ. (25MHz, PLL off) 3.3V 1.5ns typ. 2.5V 1.5ns typ. 1.8V 1.5ns typ. (200MHz, PLL off)
Output Load:	15pF
Start Up Time:	5ms max.
Output Symmetry (Duty Cycle):	50% ± 5% (measured at 50% Vdd)
Period Jitter, peak to peak:	70ps typical
Low Phase Noise (typical):	-114dBc/Hz at 1KHz offset (133MHz)
Tri-state Function Pin 1 (option):	Pin 1 = H or open.... Output active at pin 3 Pin 1 = L.... high impedance at pin 3
Output Disable/Enable Time:	150 ns
Aging:	< ± 5ppm max. / year (max.)
Reflow Condition:	260°C max. for 10 sec.

GERMANY:

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