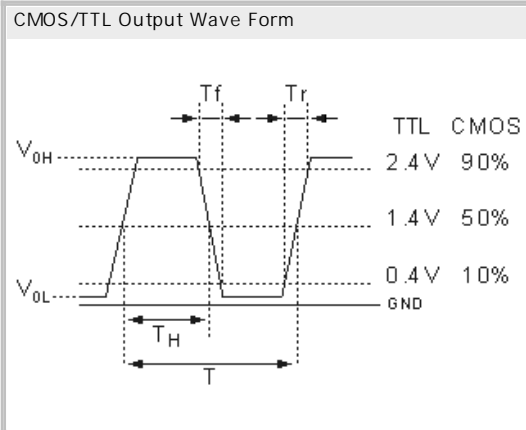
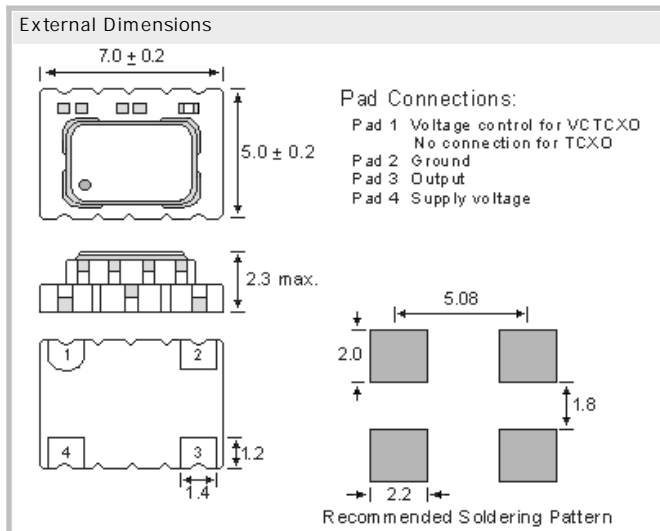


VC-TCXO-Oscillator SMD VC-TCXO7050TW3.3  
3.3V CMOS

- Voltage Control Temperature Compensated Crystal Oscillator
- SMD in ceramic case (7.0 x 5.0 x 2.3) mm
- CMOS Square Wave Output;  
Wave form code is " T "
- RoHS conform; Lead-free product; Tape & Reel
- Vibration: MIL-STD-202F method 204,  
35G, 50 to 2000 Hz
- Shock: MIL-STD-202F method 213B,  
test cond. E, 1000G 1/2 sine wave



## Specifications

Holder Type:	VC-TCXO7050TW3.3 3.3V (Voltage code is " 3.3 ")
Frequency Range:	200.01 MHz to 800.0 MHz
Standard Frequency (MHz, Partial List):	200.000, 204.800, 311.000, 400.000, 409.600, 622.080
Input Voltage:	+ 3.3V <sub>dd</sub> ± 5%
Frequency Stability:	± 0.5ppm to ± 3.0ppm
Freq. Stability vs Temperature:	0° to 50°C (± 0.5ppm) to -40 to +85°C ( ± 1.0 to ± 3.0ppm)
Freq. Stability vs Aging:	± 1.0 ppm, first year at 25°C
Freq. Stability vs Voltage Change:	± 0.3 ppm, for a ± 5% input voltage change
Freq. Stability vs Load Change:	± 0.3 ppm, for a ± 10% load condition change
Current Consumption (max.):	65 mA Frequency dependent
Output Load:	15pF
Output Logic Levels:	Logic High "1" : 90% V <sub>dd</sub> min. Logic Low "1" : 10% V <sub>dd</sub> min.
Pin 1 Options VCTCXO only:	Control Voltage: Standard +1.5V ± 1.0V for all input voltages. Slope Polarity: Positive slope: Increasing control voltage (V <sub>con</sub> ) increases output frequency Input Impedance: 2MΩ min.; Linearity ± 10% max. Modulation Bandwidth: 25kHz min.
Output Symmetry (Duty Cycle):	50% ± 5% measured at 50% V <sub>dd</sub>
Rise/Fall Time CMOS:	1.2ns typical (0.3V <-> 3.0V, 15pF load)
Start Up Time:	5.0 ms (typ.), 10.0ms ( max.) (reach 90% amplitude and at + 25°C ± 2°C)
Phase Noise (77.76MHz/155.52MHz):	-50dBc/Hz at 10Hz, -77dBc/Hz at 100Hz, -102dBc/Hz at 1kHz, -115dBc/Hz at 10kHz, -108dBc/Hz at 100kHz
Storage Temperature:	-55 to + 125°C
Reflow Condition:	260°C max. for 10 sec.

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